

FIG.2

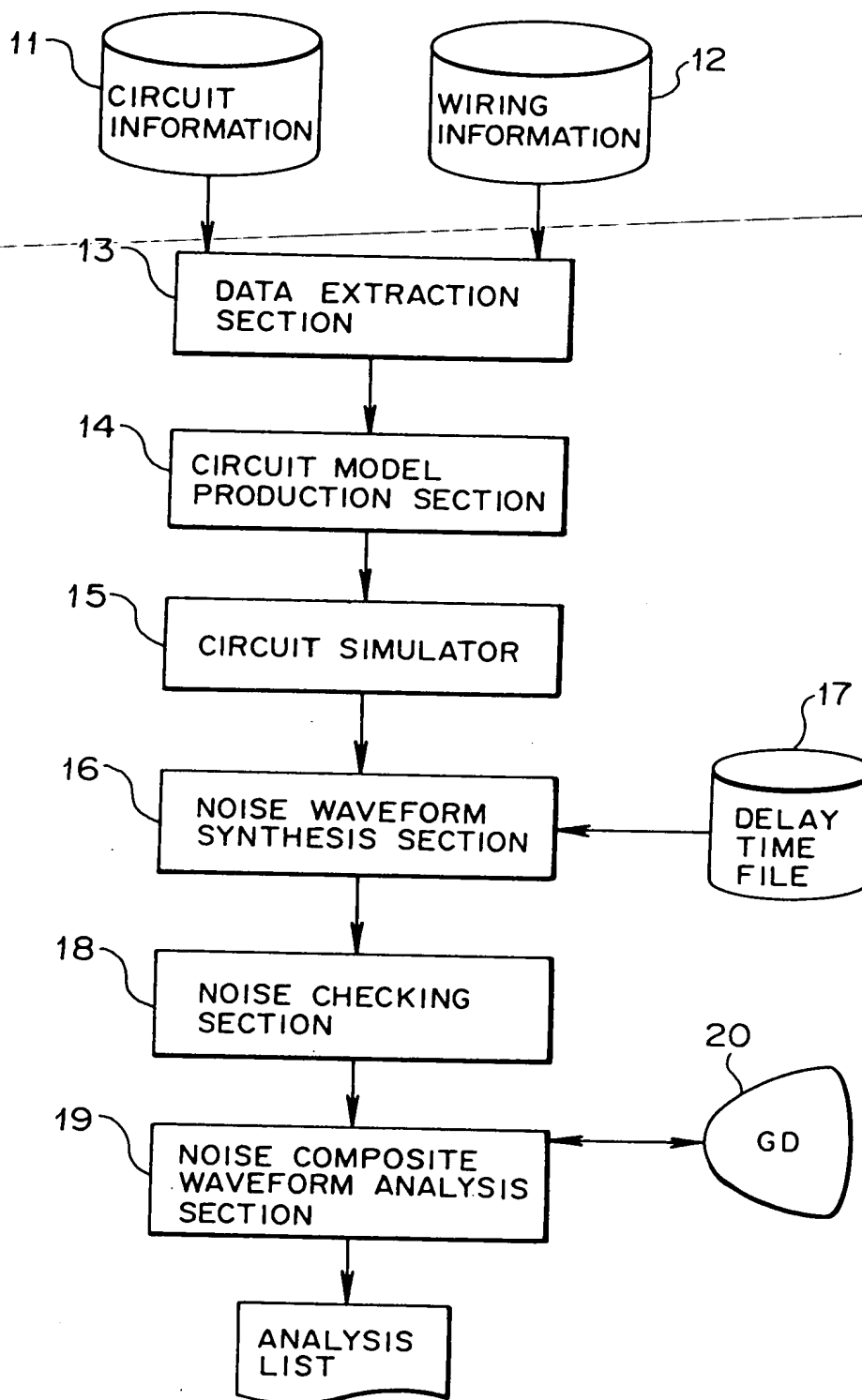
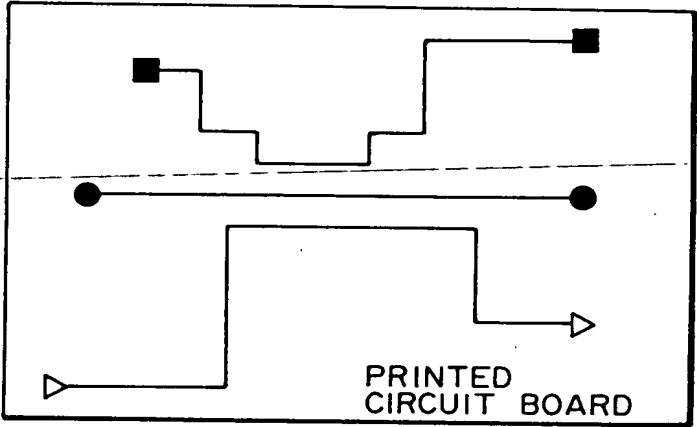


FIG.3A

Ded ,Ding NET
CONFIGURATION EXAMPLE



- — ● Ded NET (Net A)
- — ■ Ding NET (Net B)
- ▷ — ▷ Ding NET (Net C)

FIG.3B

ARRANGMENT OF
SIMULATION MODELS
(4 SIMULATIONS)

UP OF NetA (Ded)
DN OF NetA (Ded)
Net B (Ding)
Net C (Ding)

FIG.4

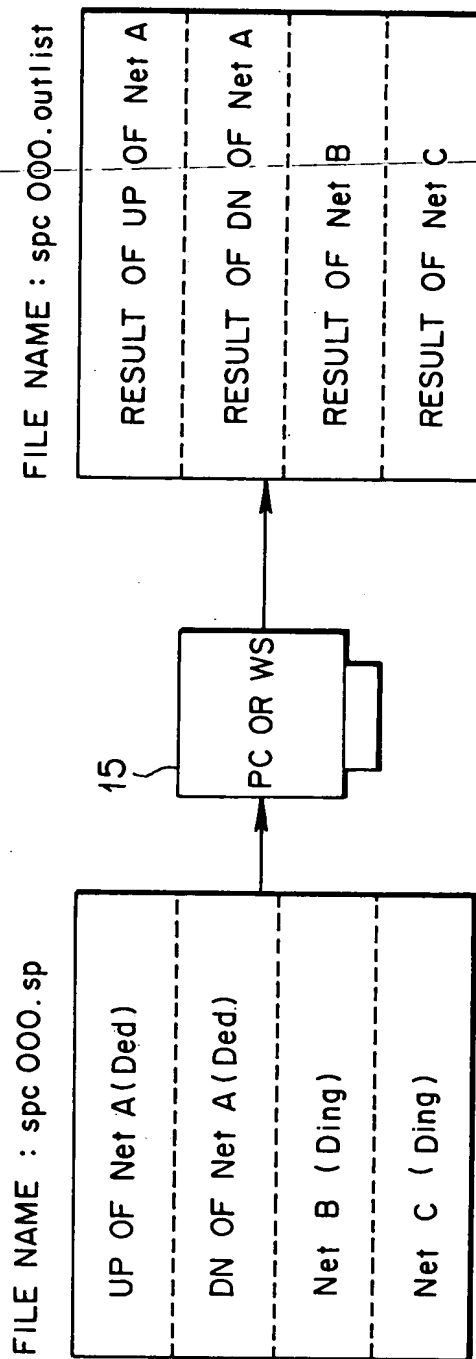


FIG.5

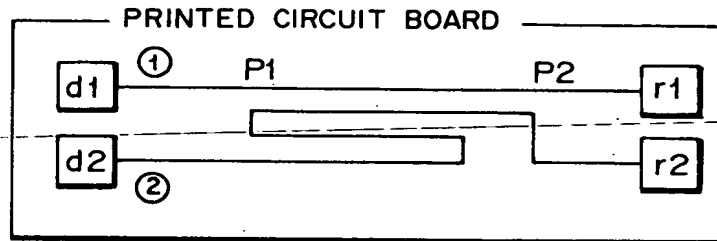


FIG.6A

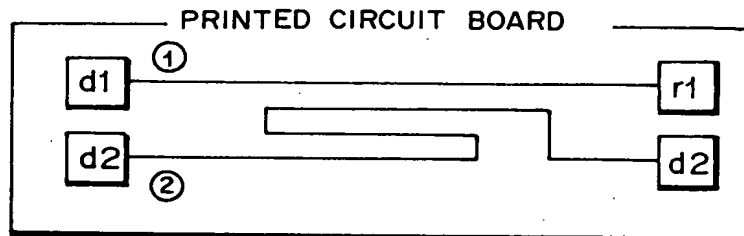


FIG.6B

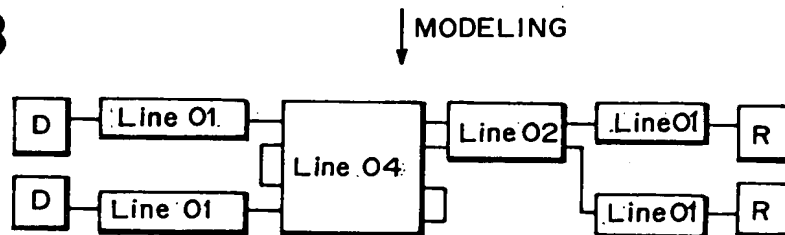
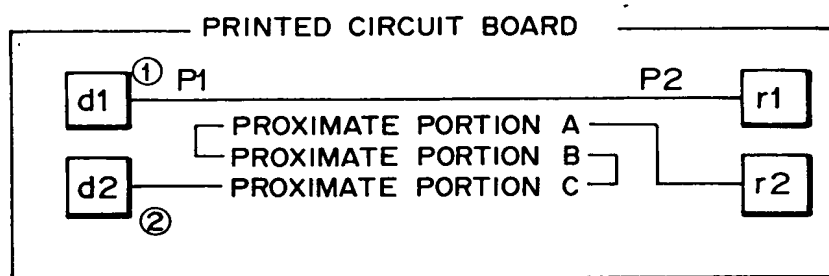


FIG. 7



035041-02433460

↓ FIRST, ONLY PROXIMATE
PORTION A IS MODELED
AS PARALLEL MODEL

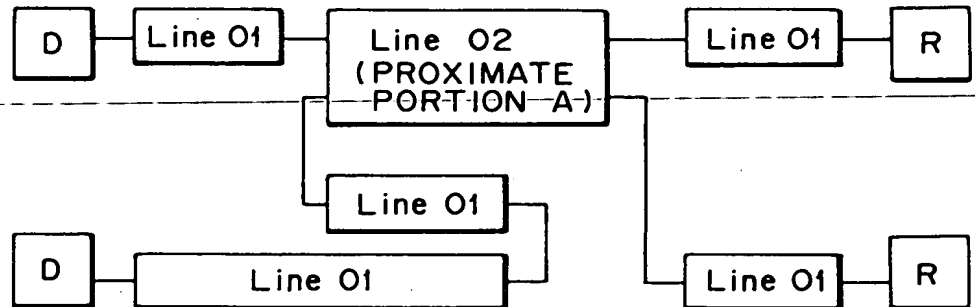


FIG. 8A

↓ NEXT, ONLY PROXIMATE
PORTION B IS MODELED
↓ AS PARALLEL MODEL

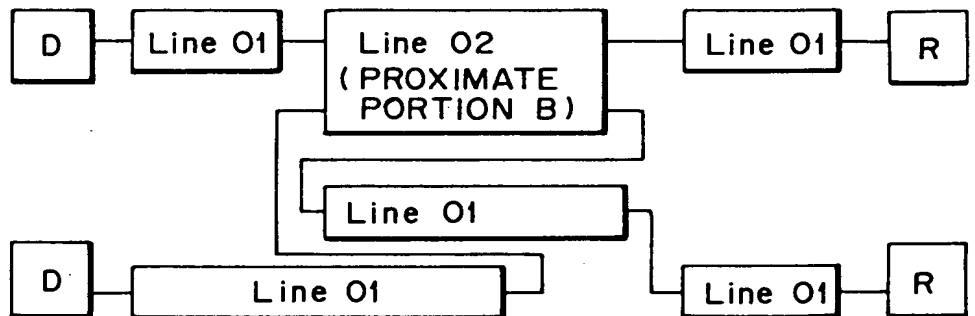


FIG. 8B

↓ FINALLY, ONLY PROXIMATE
PORTION C IS MODELED
↓ AS PARALLEL MODEL

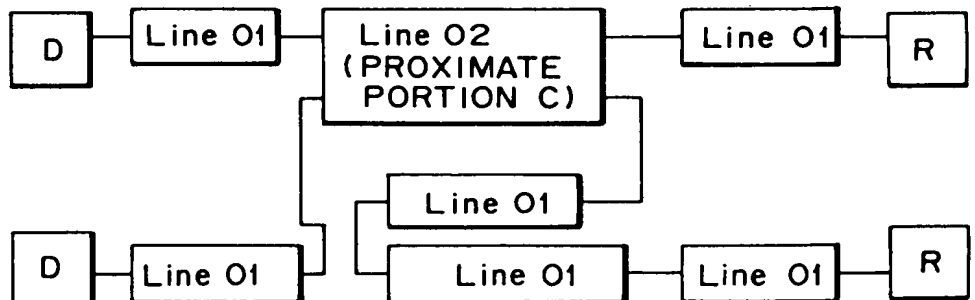


FIG. 8C

၆၆၆

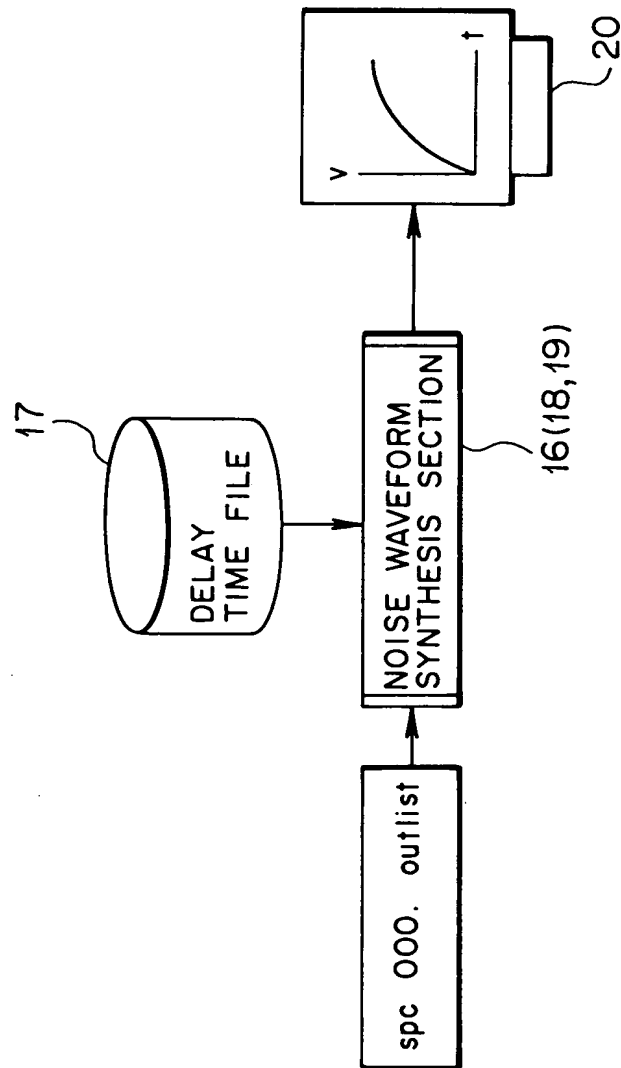


FIG. 10A

SYNCHRONOUS SW NOISE

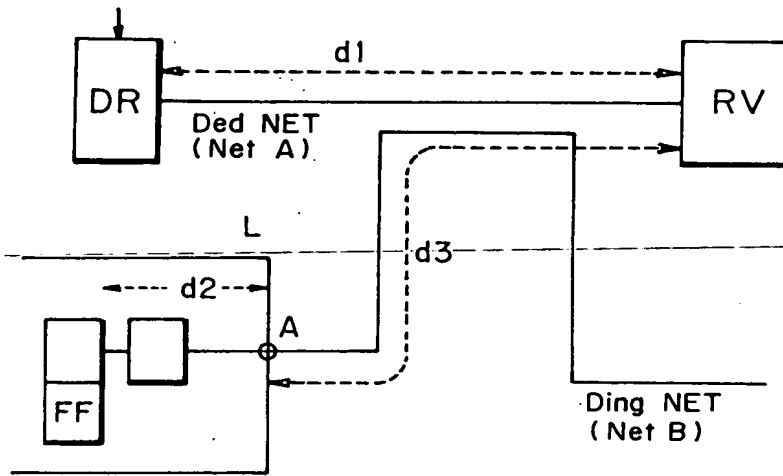


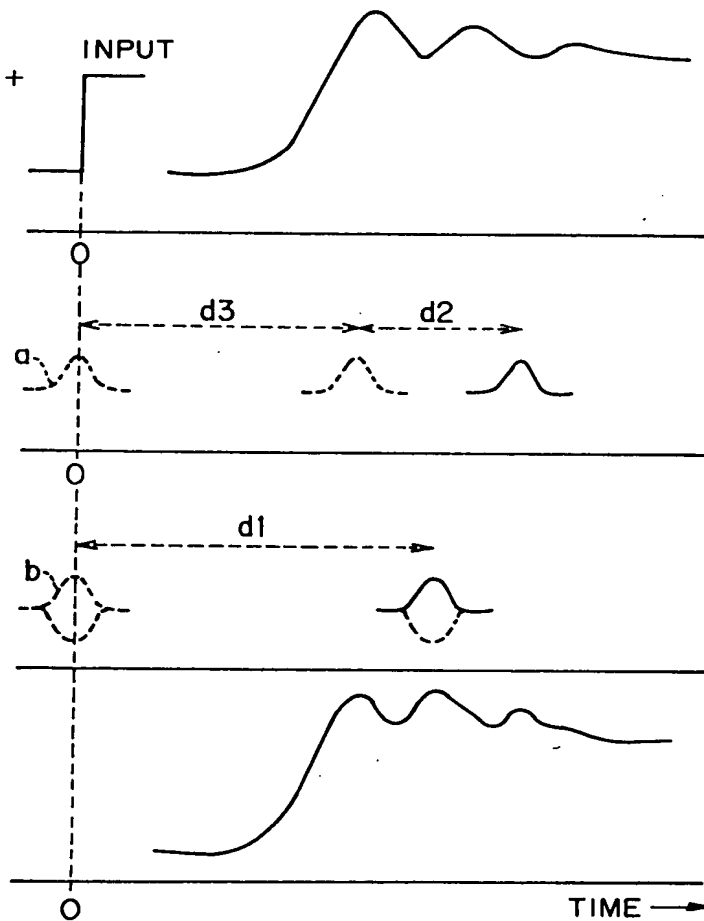
FIG. 10B

① WAVEFORM ROUNDING + REFLECTION NOISE WAVEFORM

② CROSSTALK NOISE WAVEFORM

③ SIMULTANEOUS SW NOISE WAVEFORM

④ NOISE COMPOSITE WAVEFORM



005011 02400250

FIG. 11

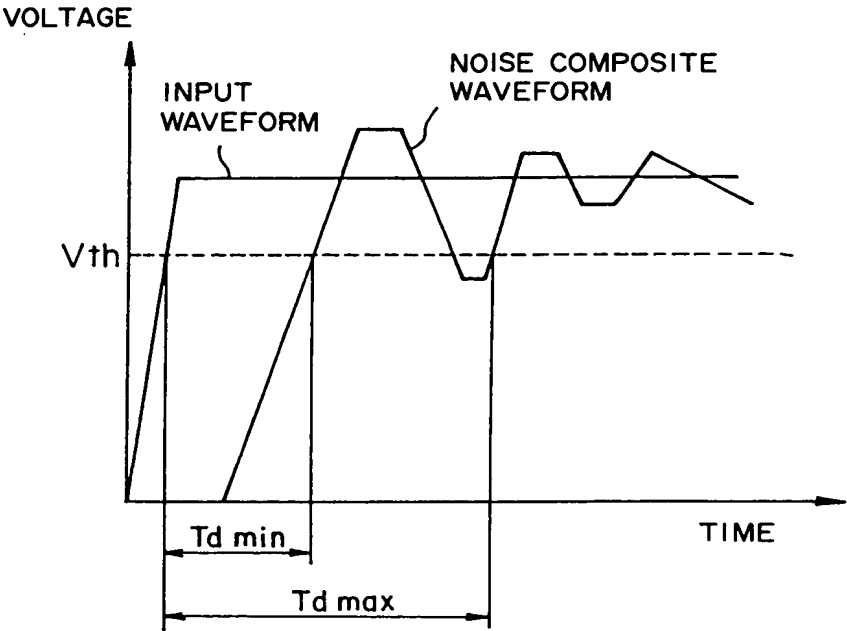


FIG. 12

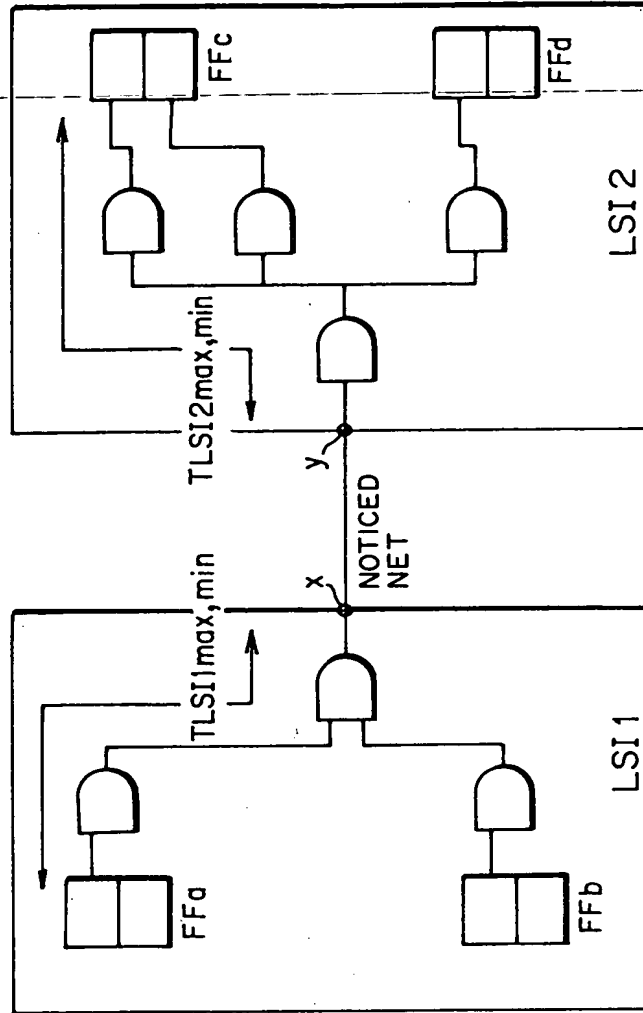


FIG. 13

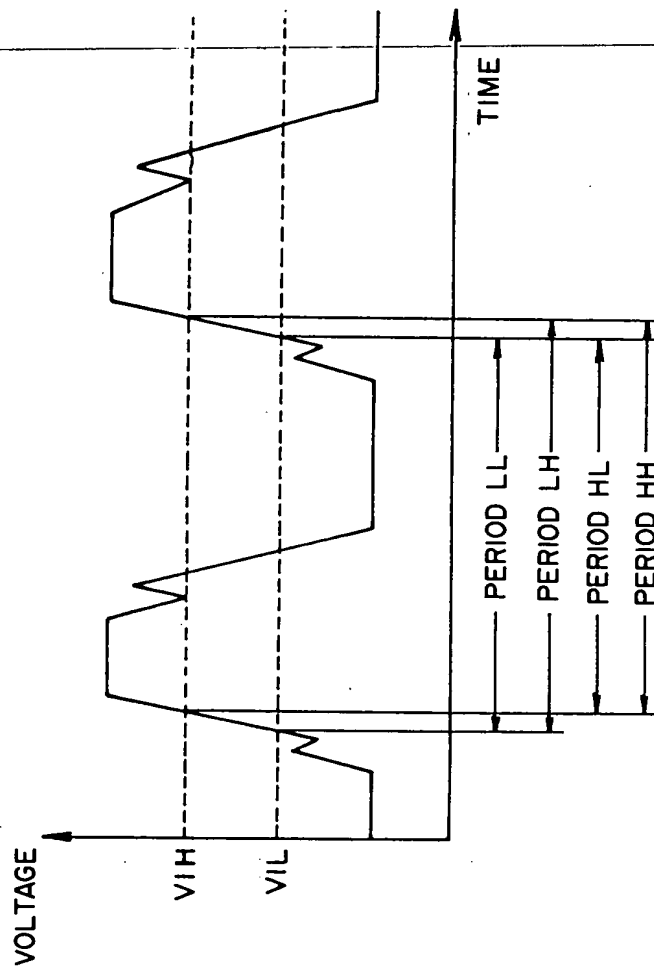


FIG.14

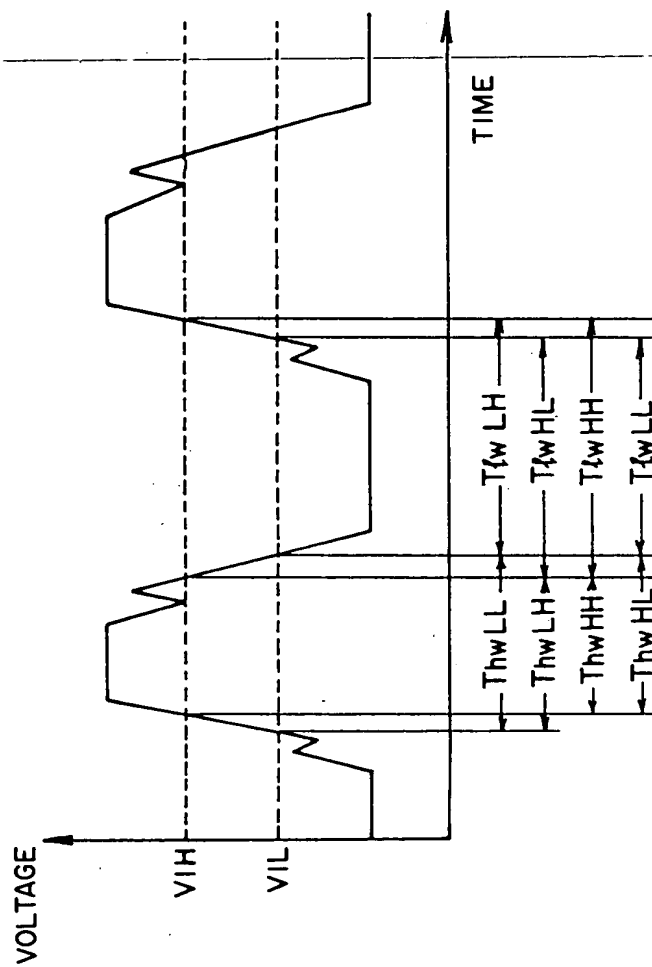


FIG. 15

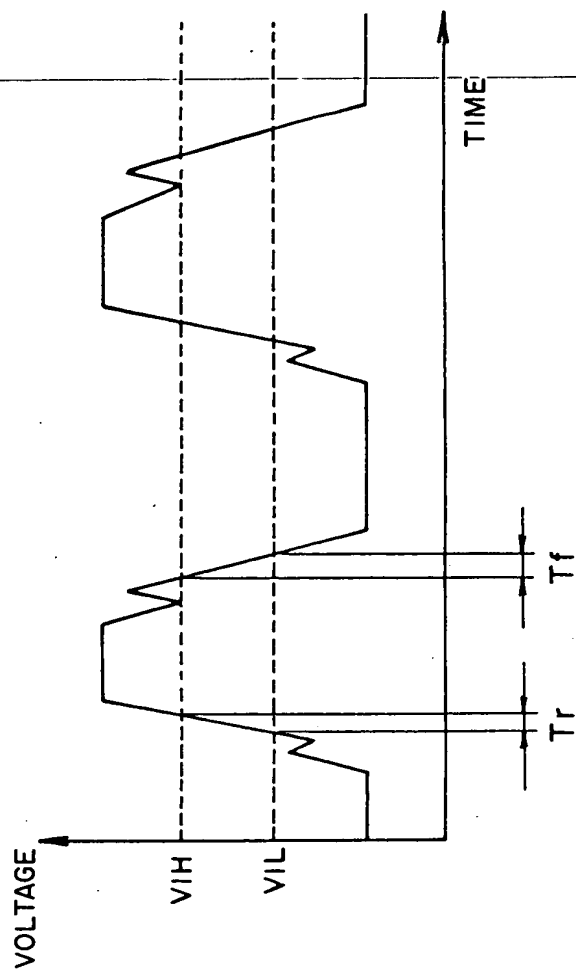


FIG. 16

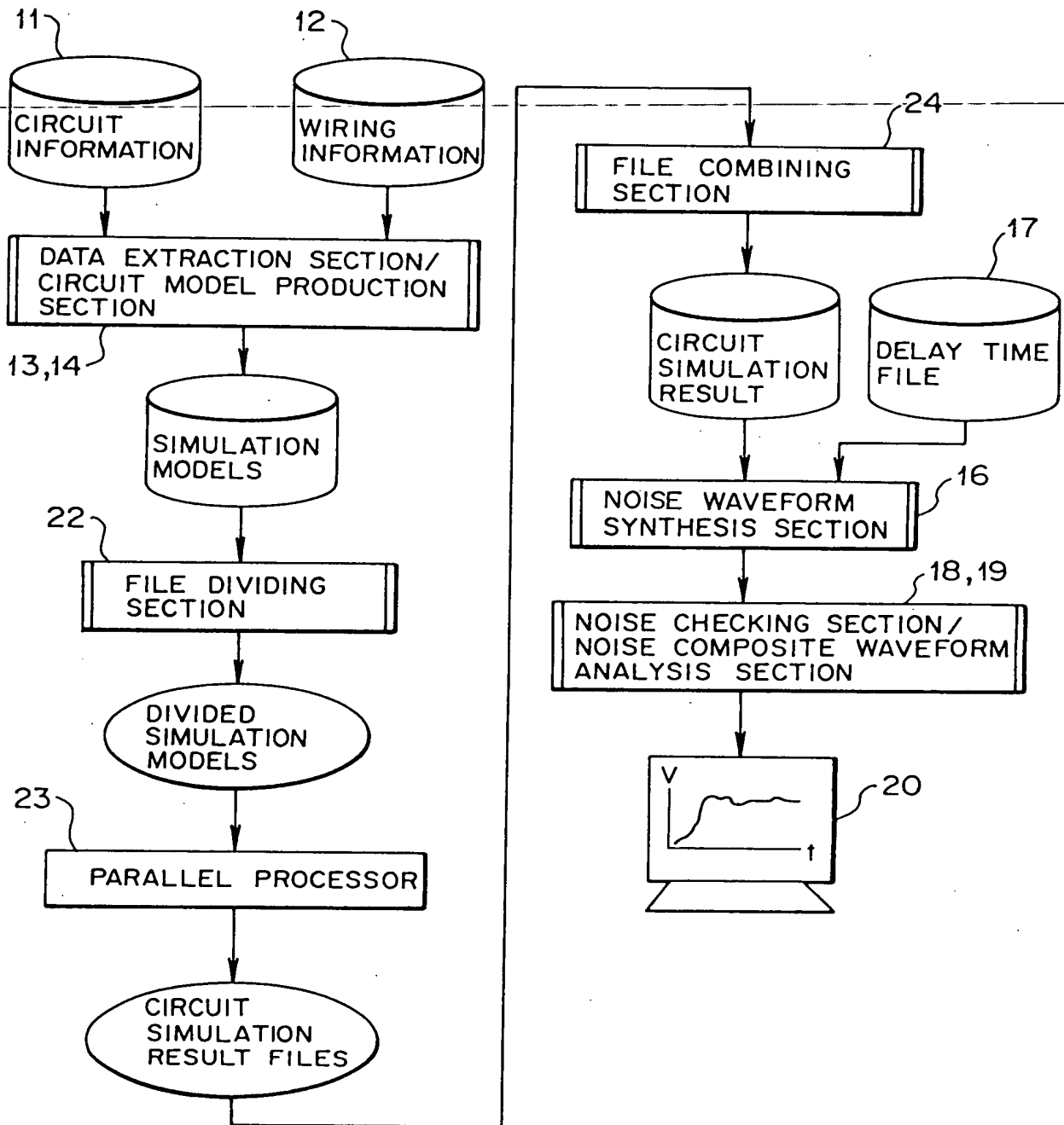


FIG. 18

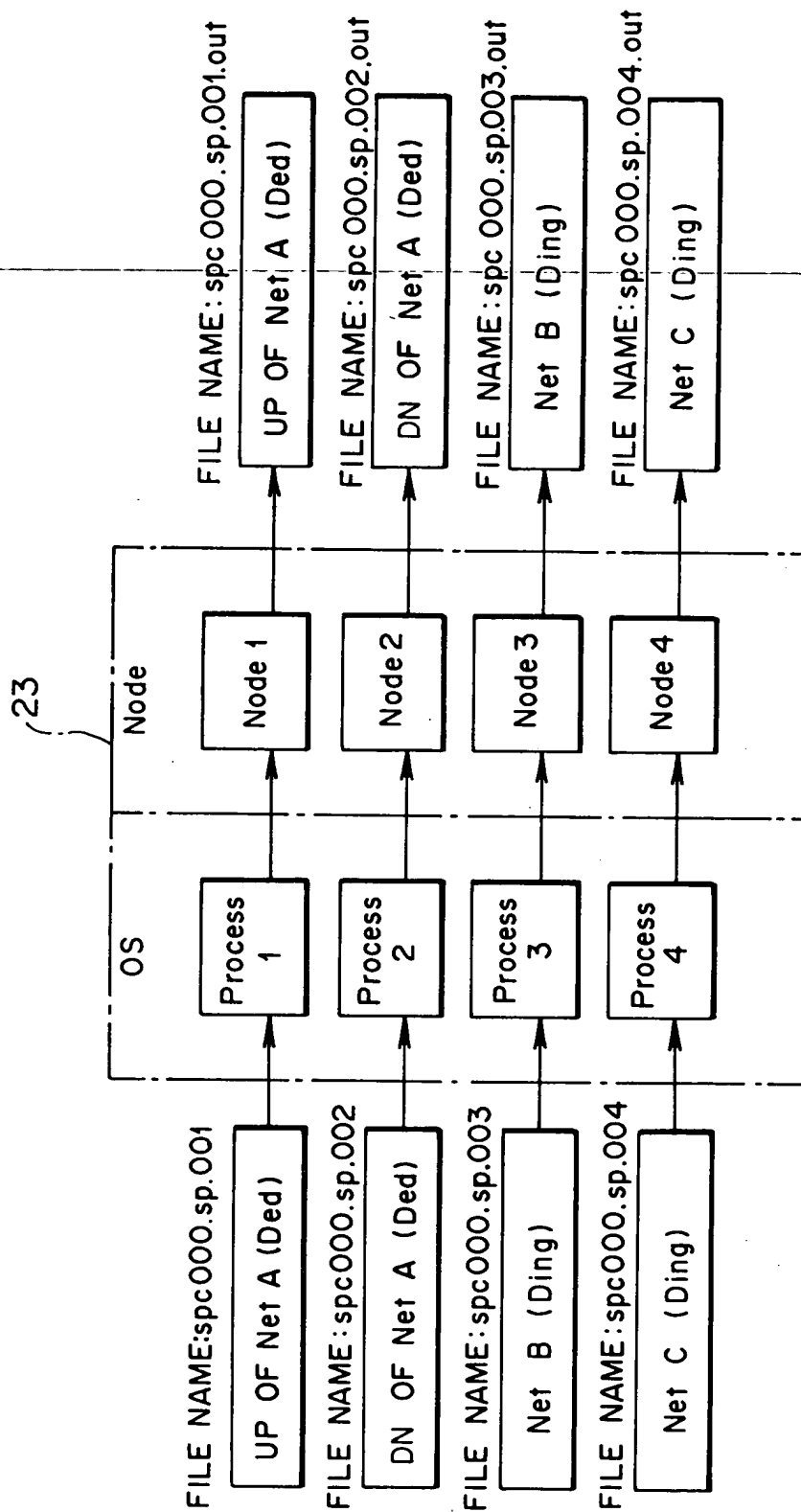


FIG. 19

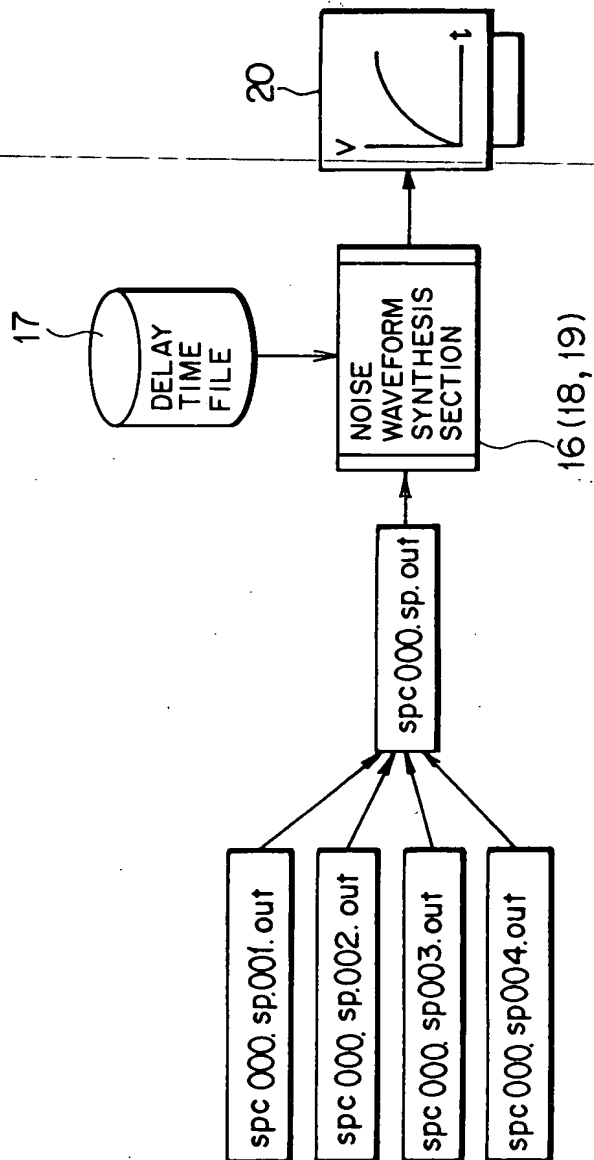
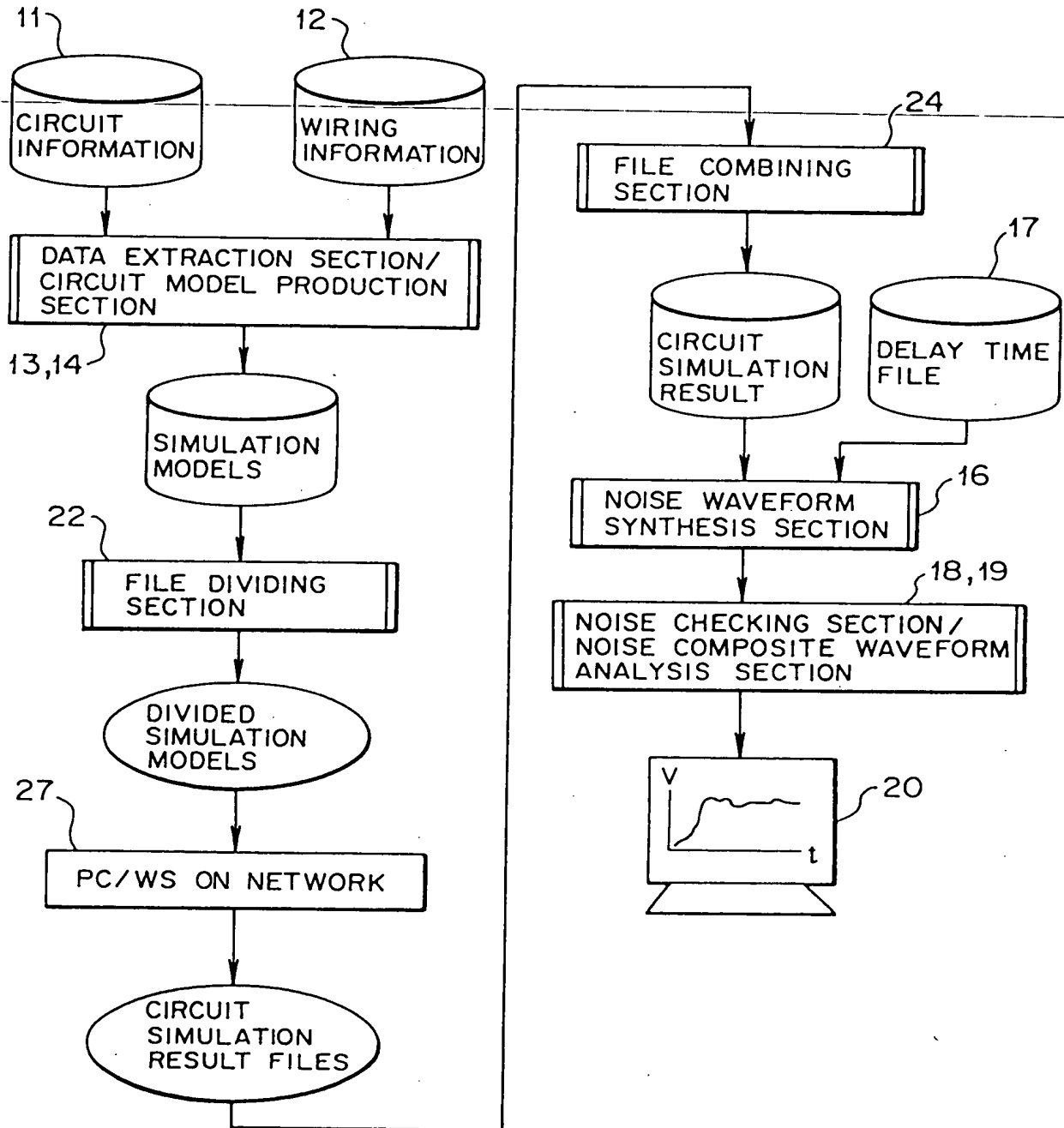


FIG.20



THE UNIVERSITY OF CHICAGO

UP OF Net A (Ded)
DN OF Net A (Ded)
Net B (Ding)
Net C (Ding)

UP OF Net A (Ded)
DN OF Net A (Ded)

Net B (Ding)
Net C (Ding)

FIG.22

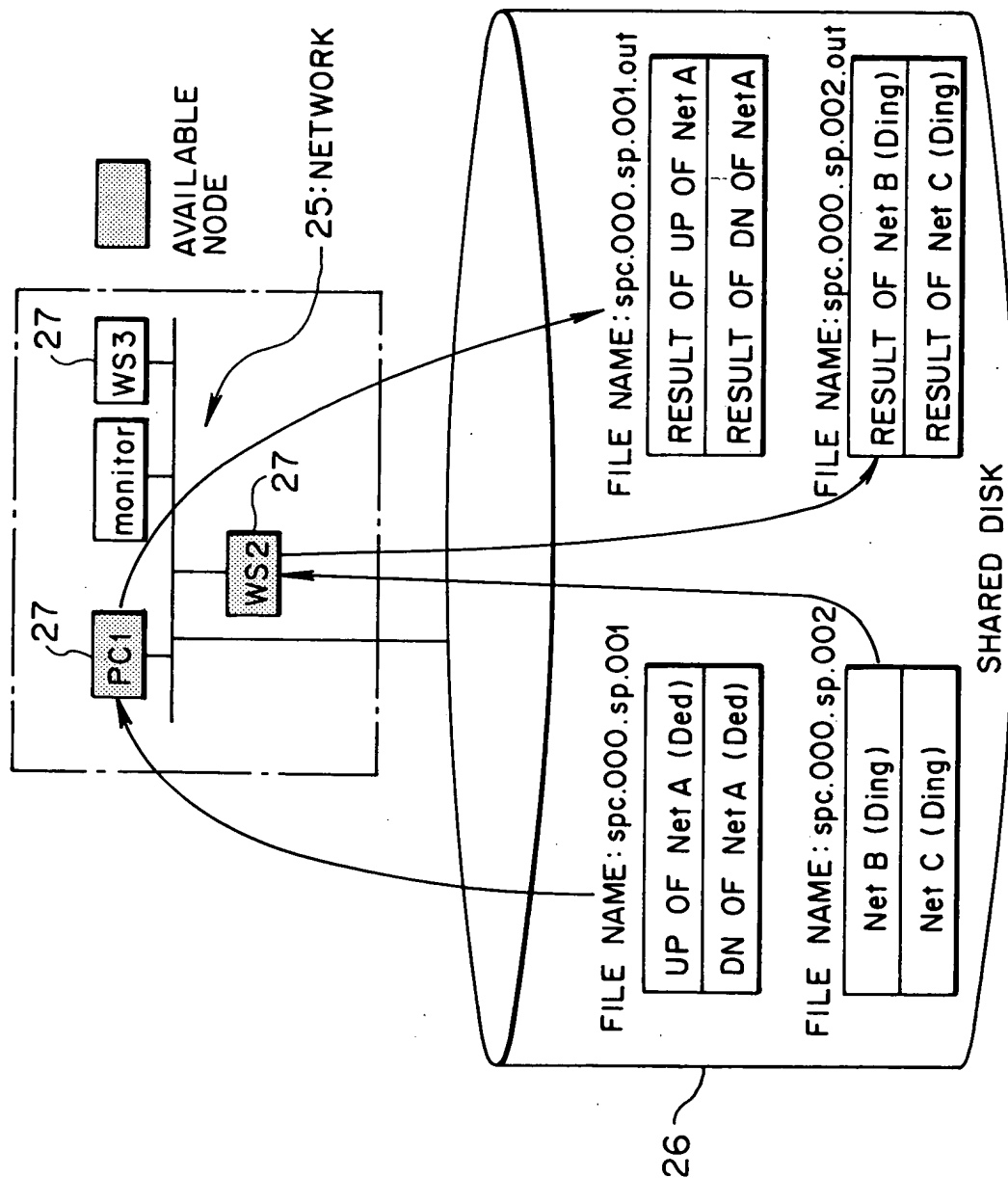


FIG.23

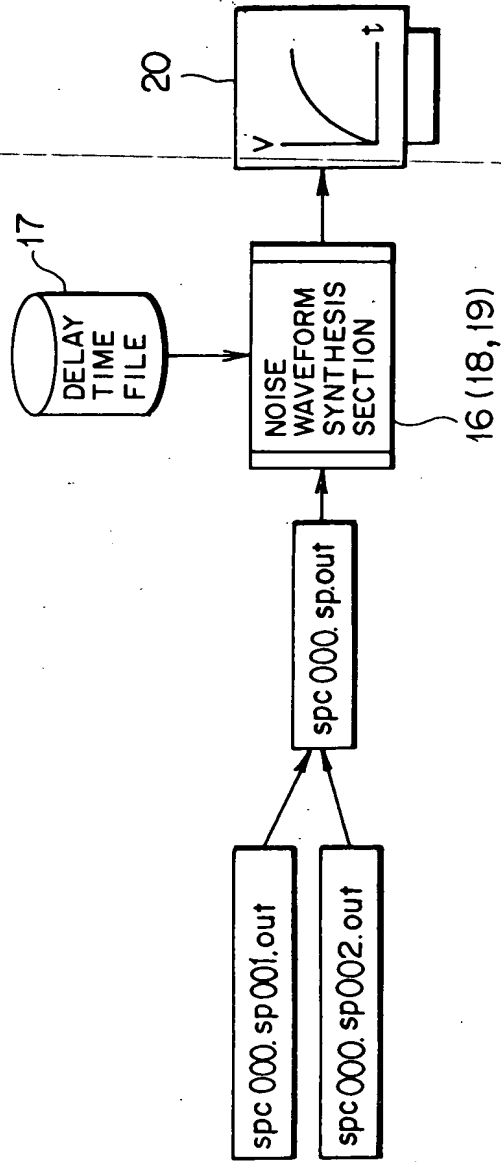


FIG.24

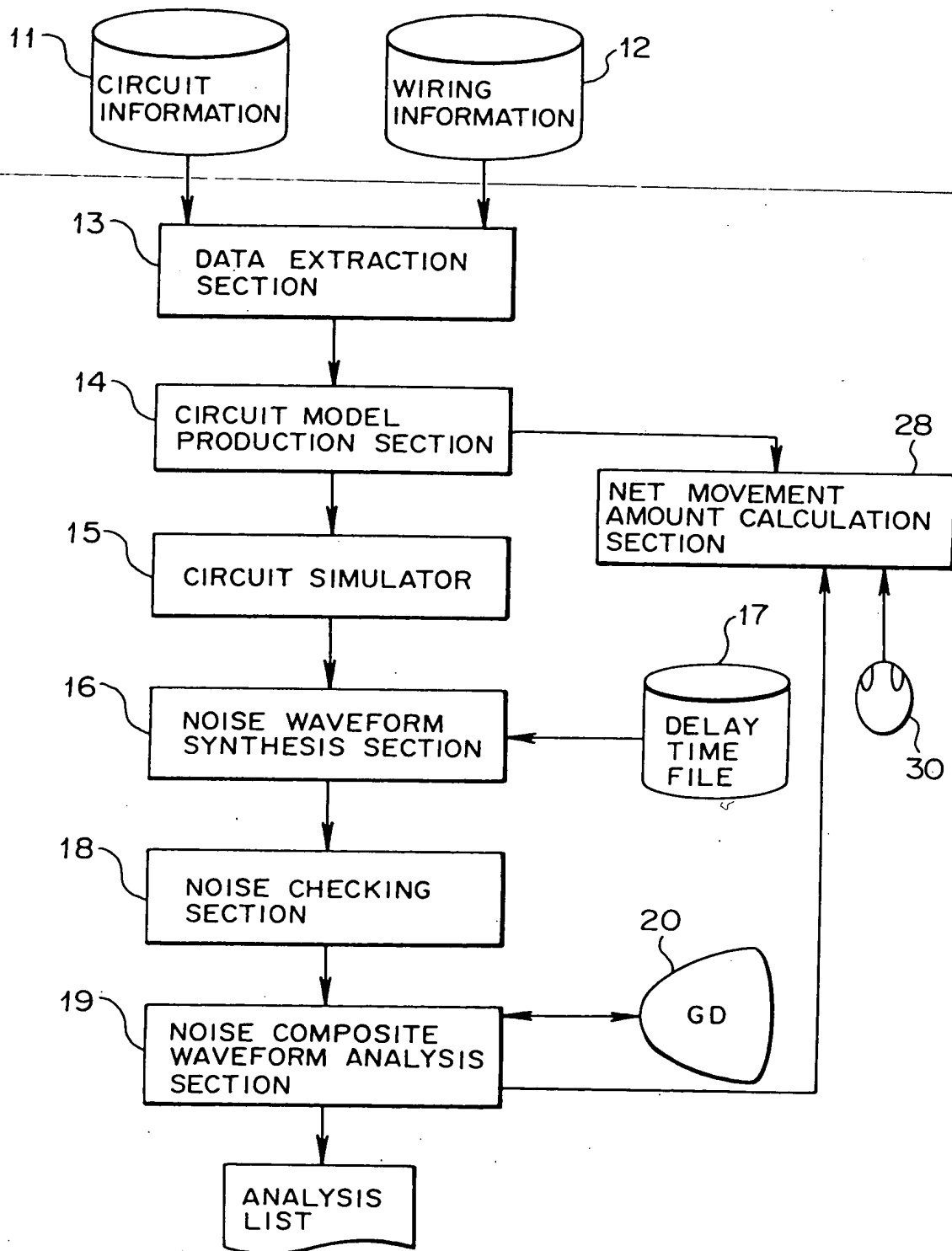


FIG. 25A

VIEW OF MOUNTING DESIGN
SYSTEM OR WIRING PATTERN

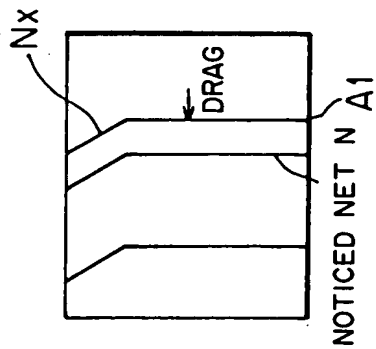


FIG. 25B

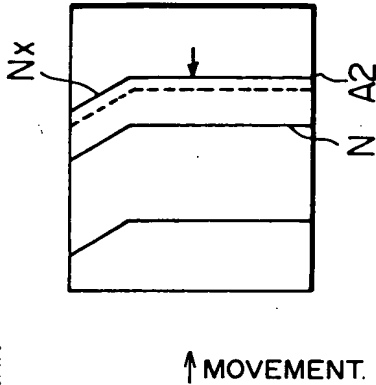


FIG. 25C

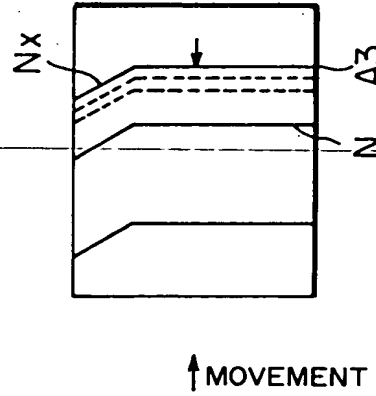


FIG. 25D

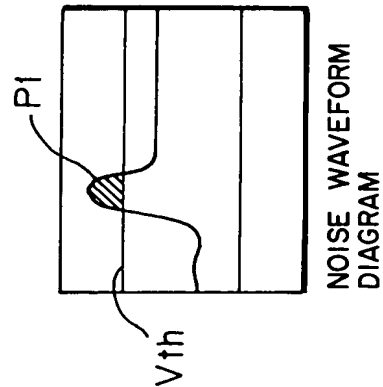


FIG. 25E

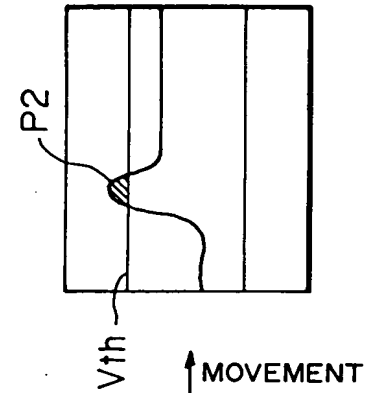


FIG. 25F

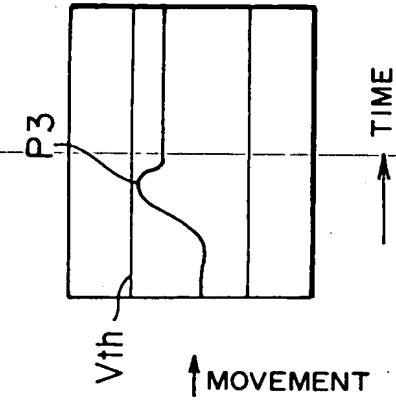
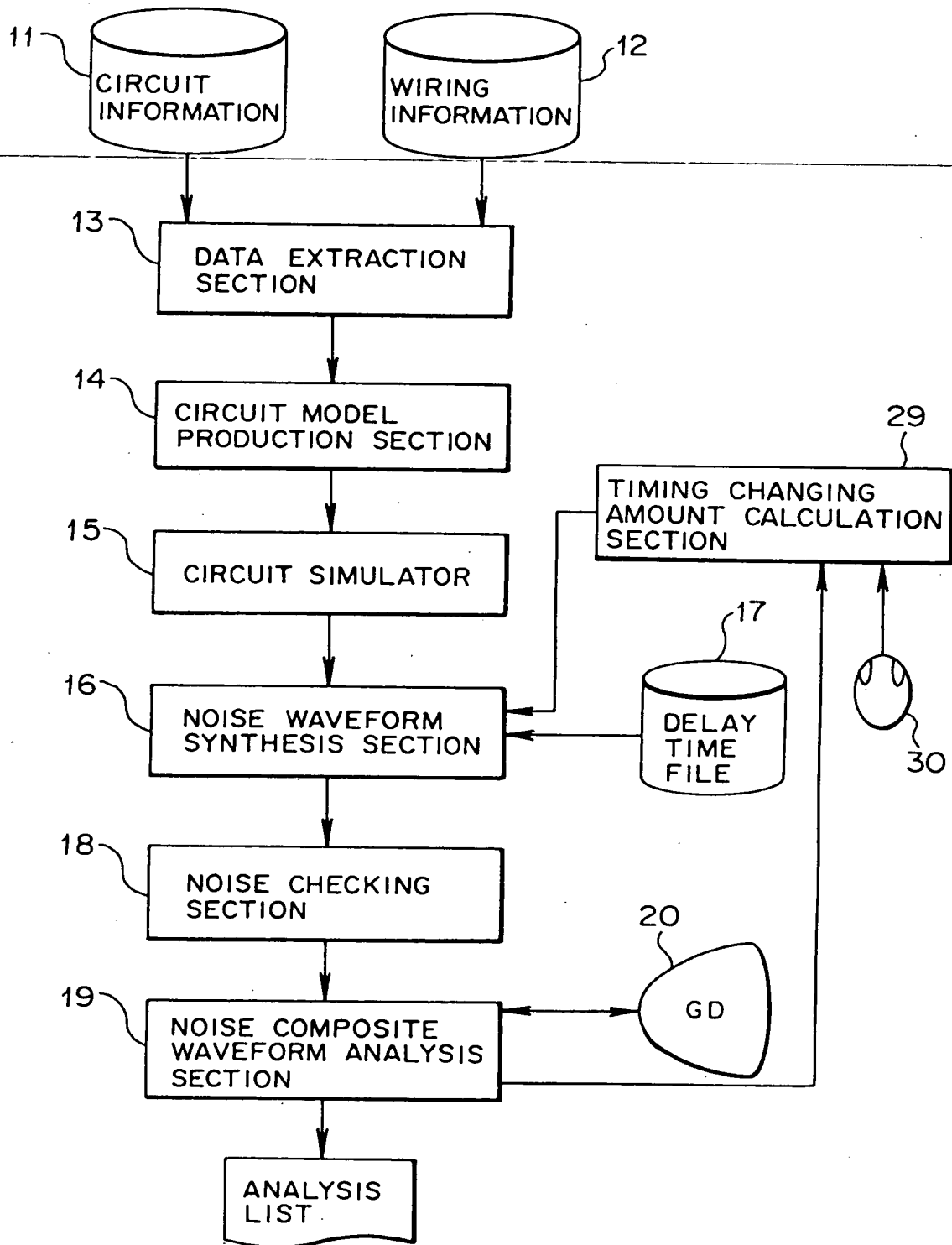
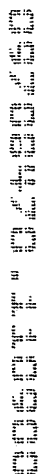


FIG. 26



Introduction



Introduction

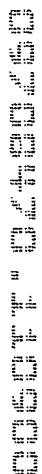


FIG. 29

RE-SYNTHESIS

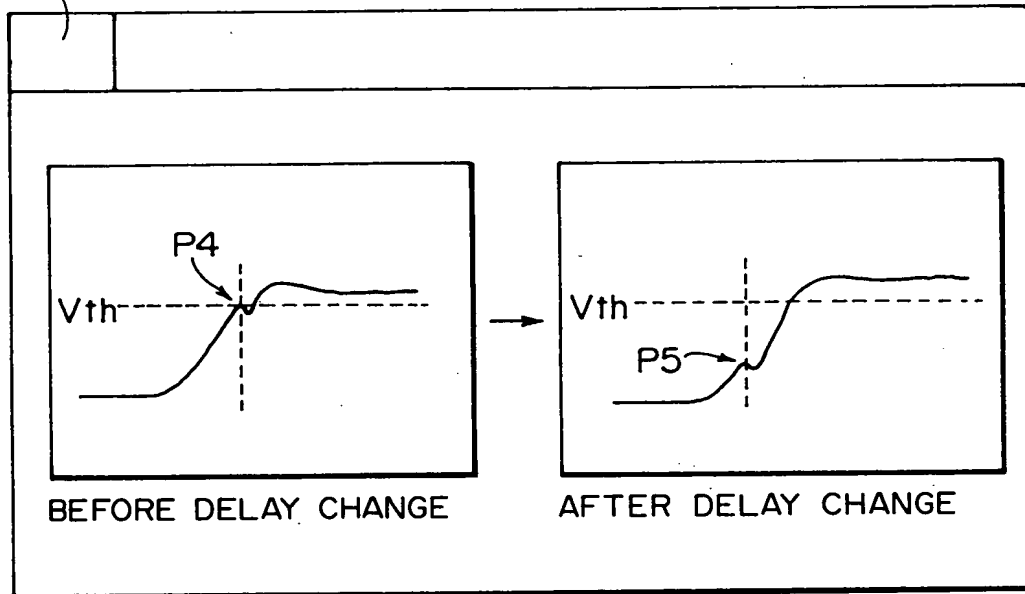


FIG. 30

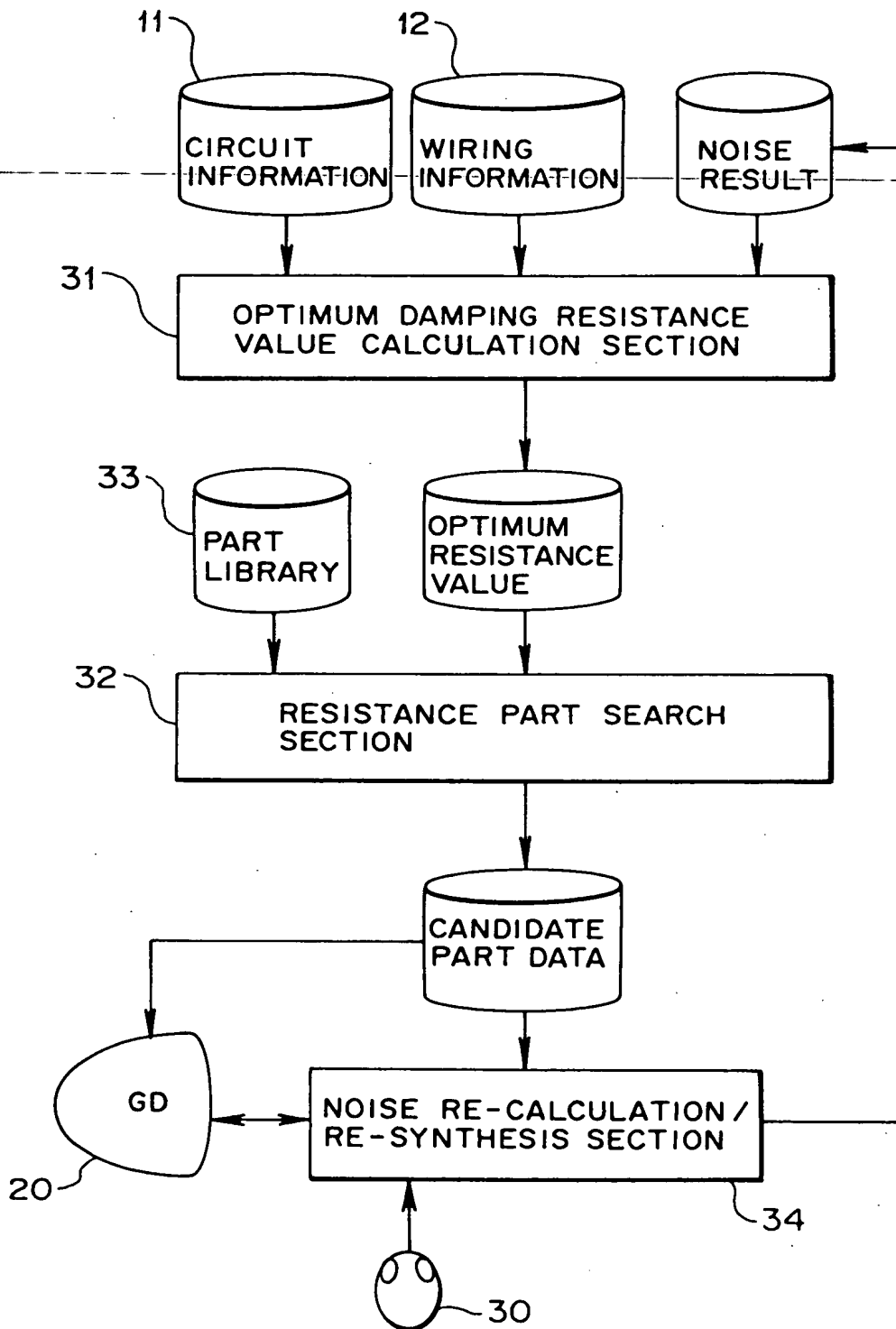


FIG. 31

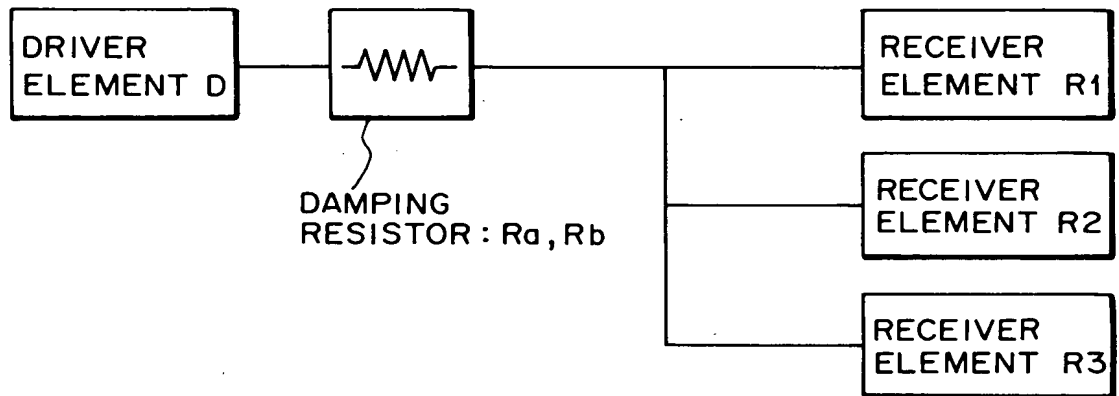


FIG. 32

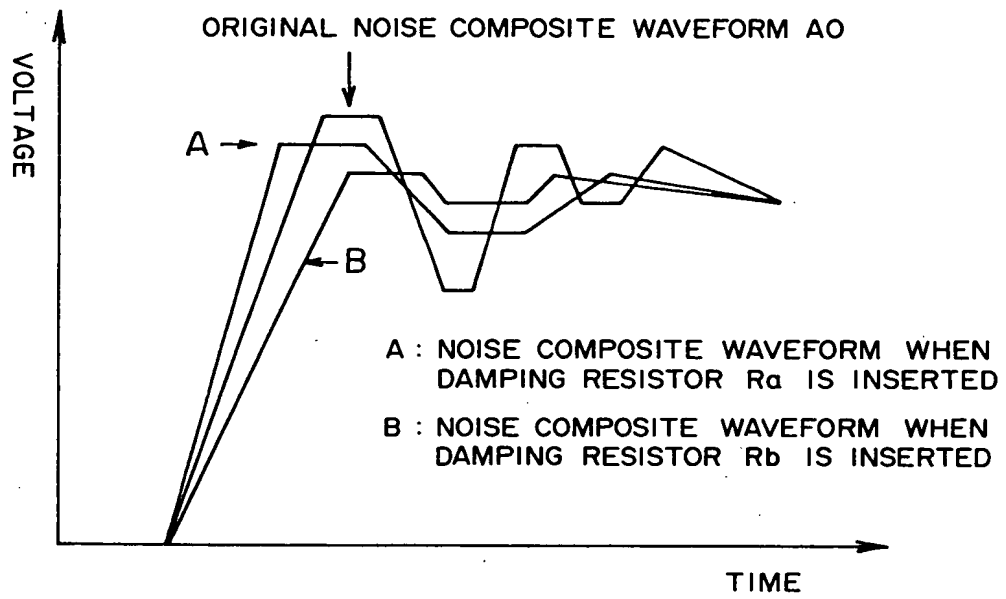


FIG. 33A

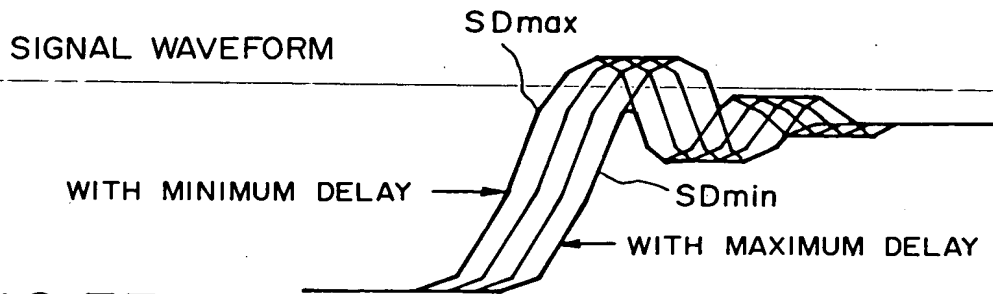


FIG. 33B

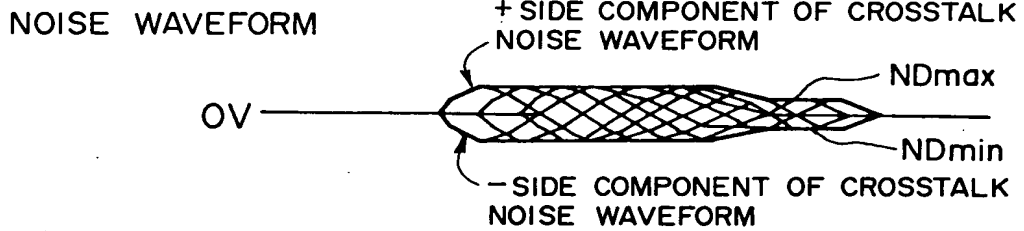


FIG. 33C

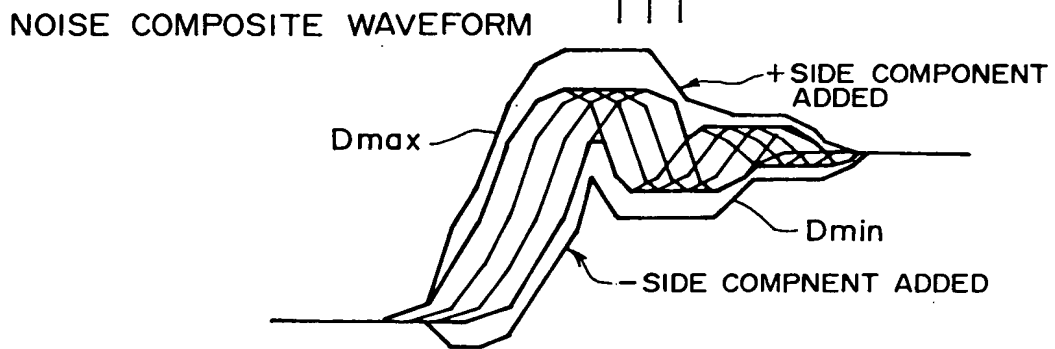


FIG. 35

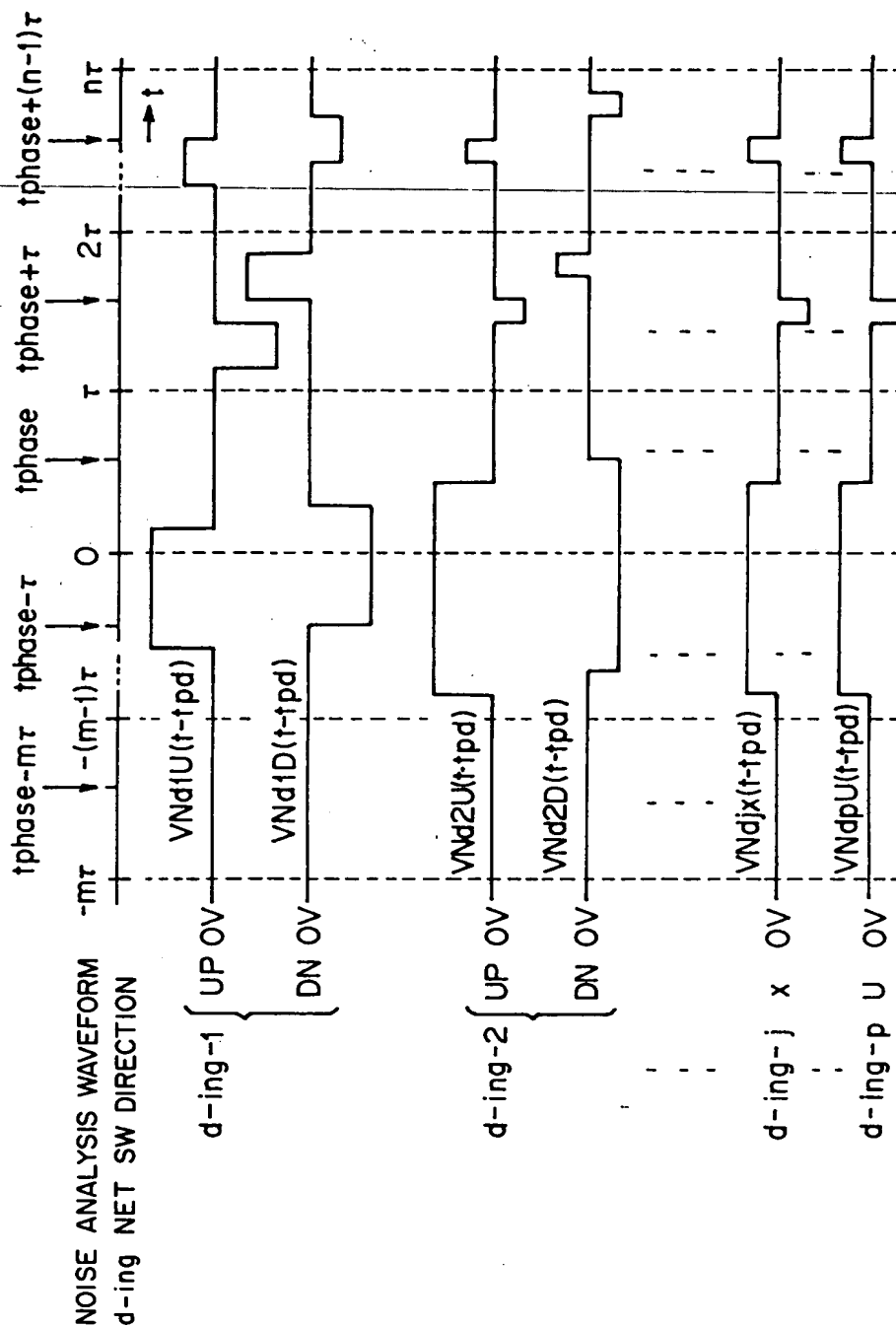


FIG. 37A

LSI DR SIMULTANEOUS SW NOISE

FIG. 37B

LSI RV SIMULTANEOUS SW NOISE

FIG. 37C

PARALLEL WIRING PATTERN CROSSTALK NOISE
+ CROSSING WIRING CROSSTALK NOISE

FIG. 37D

{ VIA CROSSTALK NOISE
TERMINATING RESISTOR SIMULTANEOUS SW NOISE
CONNECTOR CROSSTALK NOISE
CABLE CROSSTALK NOISE
DC NOISE

FIG. 37E

COMPOSITE NOISE WAVEFORM

FIG. 37F

TRANSMISSION WAVEFORM OF NOTICED NET
(UPON RISING)

FIG. 37G

COMPOSITE TRANSMISSION WAVEFORM OF NOISES
OF NOTICED NET (NOISE COMPOSITE WAVEFORM)

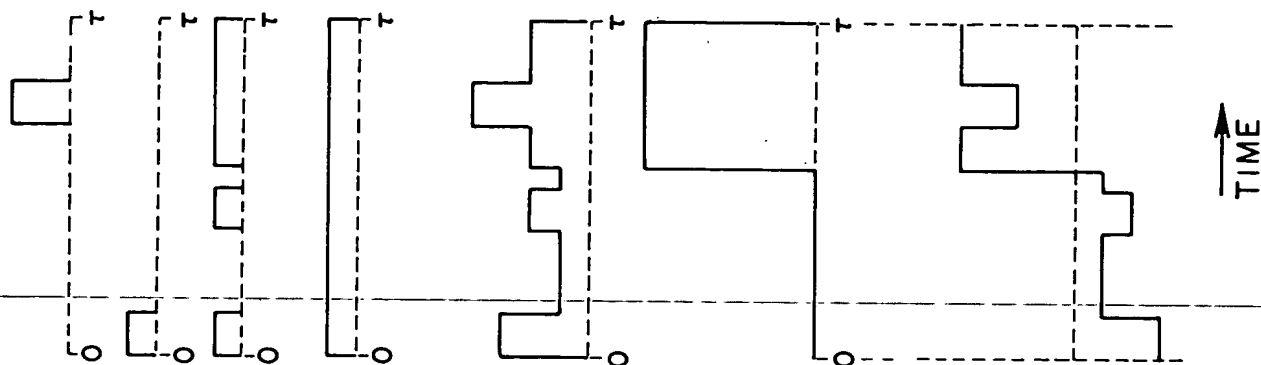


FIG. 39A

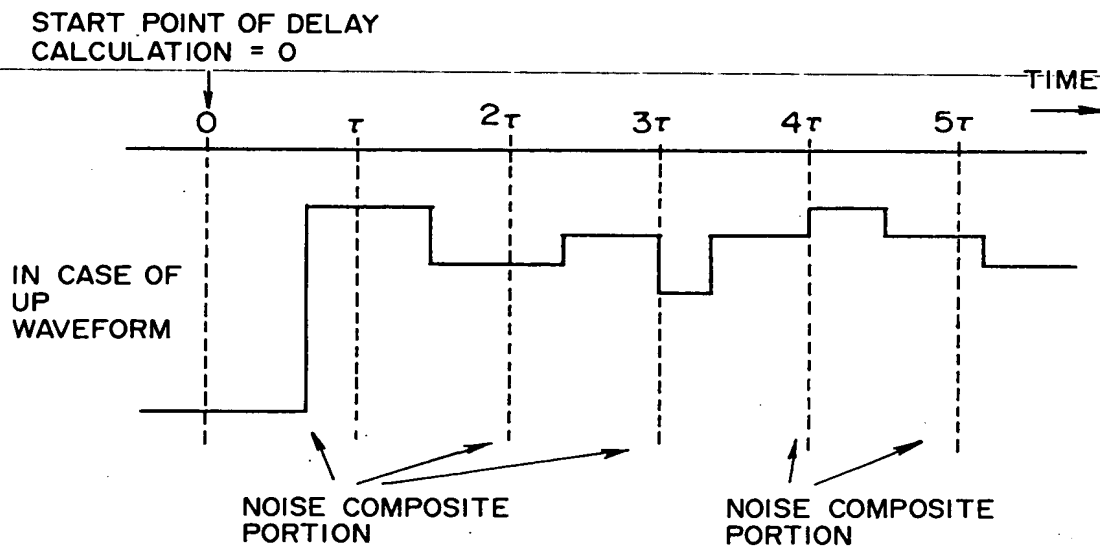


FIG. 39B

COMPRESSED NOISE
WAVEFORM
(- SIDE IS USED HERE)

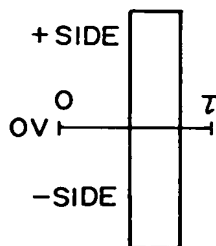


FIG. 41

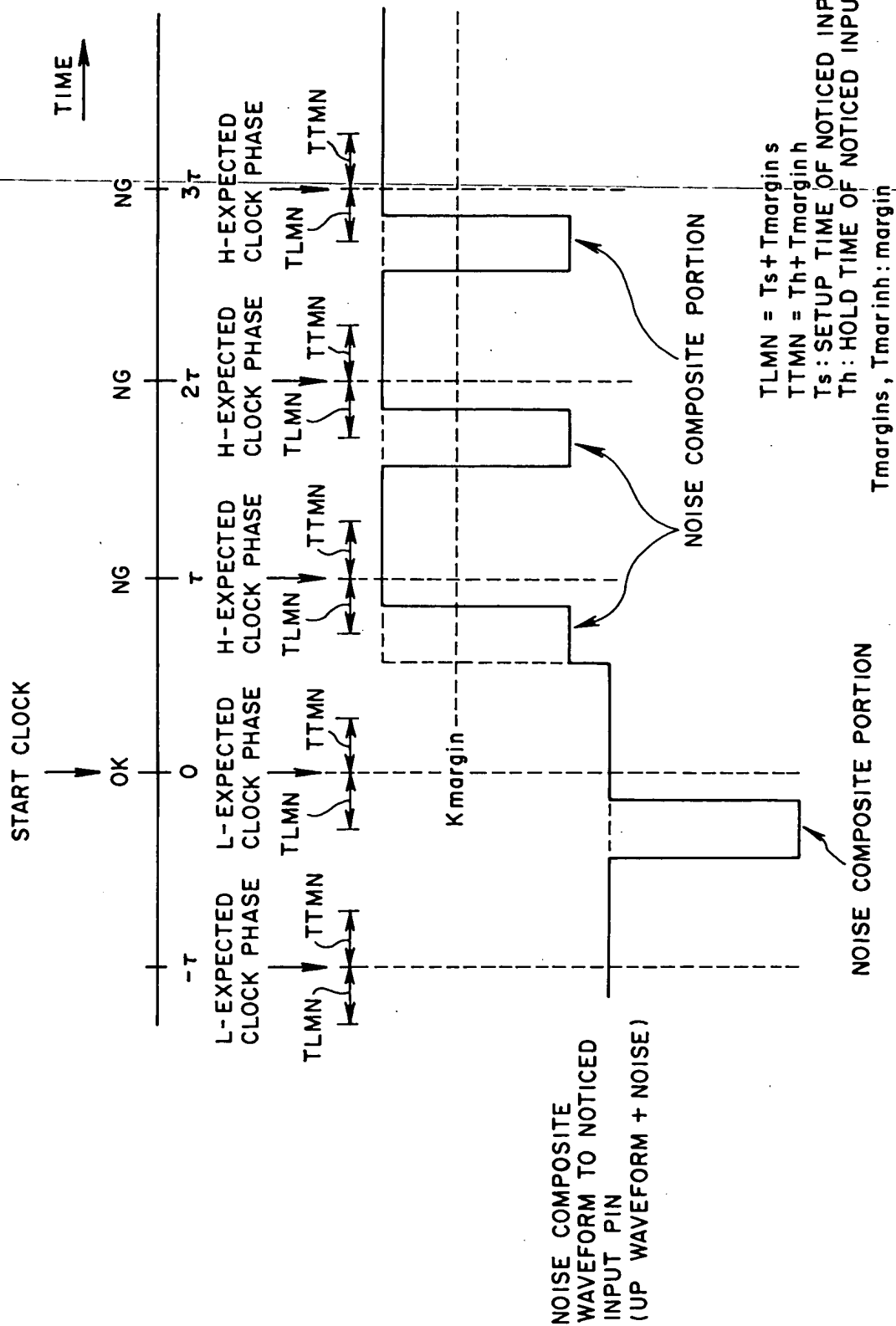


FIG. 42A

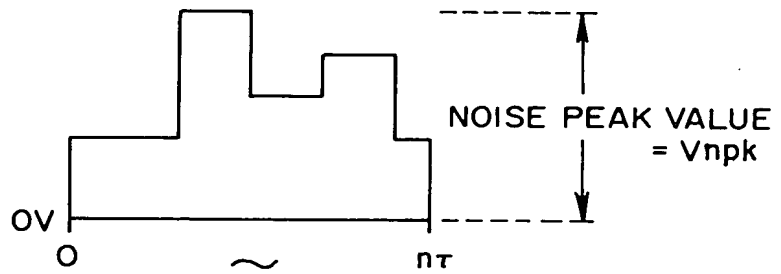


FIG. 42B

